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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n Re Patent Application of:

Khellah et al.

Examiner: Andrew Q. Tran

Application No.: 10/810,093

Art Unit: 2824

Filed:

FES 0 9 2006

March 26, 2004

For:

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Substitute for Form 1449/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Complete if Known						
					Application Number			10/810,093			
					Filing D	ate		March 26, 2004 Khellah et al.			
					First Na	med Inventor:					
	(use as many sheets as necessary)						Art Unit 2824				
/ ETD 2 0 0							er Name		Andrew Q. Tran		
Sheet	UQ5 ,,		of	1		Attorne	y Docket Num	ber	42P16596		
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Examiner Initials*	Cite No <sup>1</sup>	NON PATENT LITERATURE DOCUMENTS  Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published									
	1.	WANN, C. et al., "CMOS with Active Well Bias for Low-Power and RF/Analog Applications' IEEE, Symposium on VLSI Technology Digest of Technical Papers, 2000, pages 158-159.									
	2.		AGARWALA, A. J. et al., "The Impact of Intrinsic Device Fluctuations Cell Stability", IEEE, Journal of Solid-State Circuits, April 2001, pages 4.								
	3.	BHAVNAGARWALA, A. J. et al., "Dynamic-Threshold CMOS SRAM Cells for Fast, Portable Applications", IEEE, 2000, pages 359-363.									
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